

KOREAN PATENT ABSTRACTS

(11)Publication number: 1020020071542 A
(43)Date of publication of application: 13.09.2002

(21)Application number: 1020010011618
(22)Date of filing: 07.03.2001
(30)Priority:

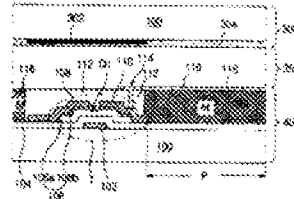
(71)Applicant: LG.PHILIPS LCD CO., LTD.
(72)Inventor: KIM, UNG GWON
LEE, JONG HUN

(51)Int. Cl. G02F 1/1335

(54) LCD WITH INK JET TYPE COLOR FILTER AND FABRICATING METHOD THEREOF

(57) Abstract:

PURPOSE: A liquid crystal display with an ink-jet type color filter and a method for fabricating the same are provided to form an ink partition wall protecting layer with a transparent organic material for improving the reliability while reducing the processing steps. CONSTITUTION: A liquid crystal display with an ink-jet type color filter includes upper and lower substrates(300,400) facing to each other, a liquid crystal layer (350) interposed between the upper and lower substrates, a plurality of gate and data wires intersecting each other on the lower substrate, thin film transistors(T) positioned in the intersection areas between the gate and data wires, an ink partition wall protecting layer(112) positioned on the data lines and the thin film transistors and formed of transparent organic material with drain contact holes for exposing drain electrodes(110) of the thin film transistors partially, ink-jet type color filter(116) formed with R, G, and B color sections by the ink partition wall protecting layer by pixel area(P) units defined by the intersection between the gate and data lines, and pixel electrodes(118) positioned on the color filter and connected to the drain electrodes via the drain contact holes.



copyright KIPO 2003

Legal Status

- Date of request for an examination (20010307)
- Notification date of refusal decision (00000000)
- Final disposal of an application (registration)
- Date of final disposal of an application (20030828)
- Patent registration number (1003976710000)
- Date of registration (20030829)
- Number of opposition against the grant of a patent ()
- Date of opposition against the grant of a patent (00000000)
- Number of trial against decision to refuse ()
- Date of requesting trial against decision to refuse ()
- Date of extinction of right ()

110 : 印刷日期
 120 : 印刷日期
 130 : 印刷日期
 140 : 印刷日期

110 : 印刷日期
 120 : 印刷日期
 130 : 印刷日期
 140 : 印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

印刷日期

... (transcription of the dense, illegible text) ...

附录 1

... (transcription of the dense, illegible text in the appendix) ...

... ..
... ..

... ..

解法 1.

... ..
... ..
... ..
... ..
... ..
... ..
... ..
... ..
... ..

解法 2.

... ..
... ..
... ..
... ..

解法 3.

... ..
... ..

解法 4.

... ..
... ..
... ..

解法 5.

... ..
... ..
... ..

解法 6.

... ..
... ..

解法 7.

... ..
... ..
... ..

解法 8.

... ..
... ..
... ..

도 17.

도 15 중 도면 15 중 도면 15의 상태에 있어서,

상기 금속층을 건식식각(dry etching)을 이용하여 불꽃처리 온도 및 방식 및 절연시감치용 하부기판의 제조방법.

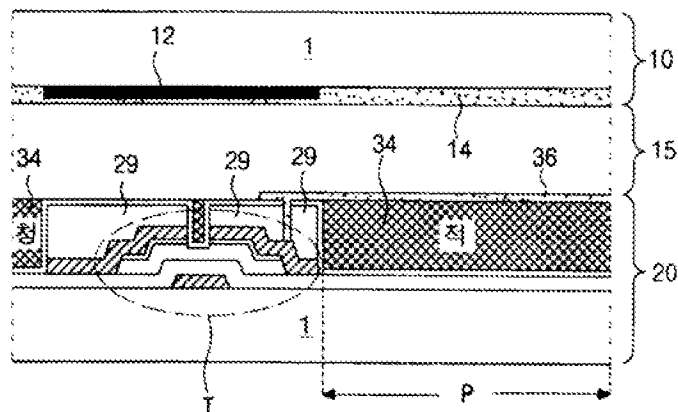
도 18.

도 15 중 도면 15 중 도면 15의 상태에 있어서,

상기 절연층을 금속층이 형성된 절연층(Resin)의 온도 및 방식 및 절연시감치용 하부기판의 제조방법.

도 19.

도 19a



도 19b

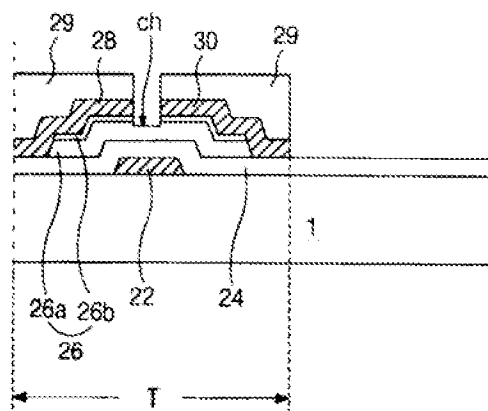


圖 2b

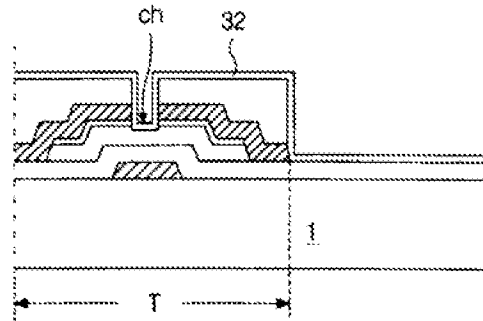


圖 2c

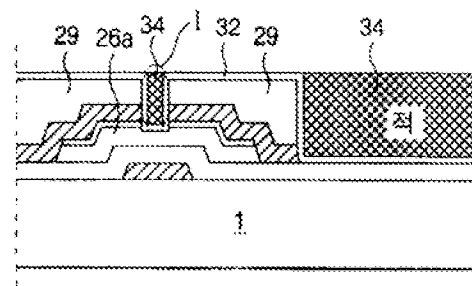


圖 2d

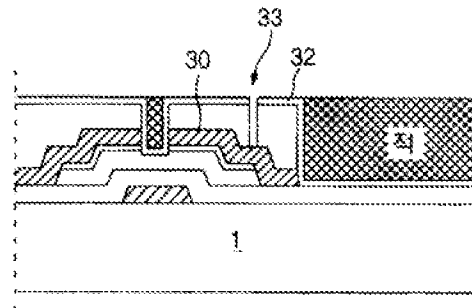
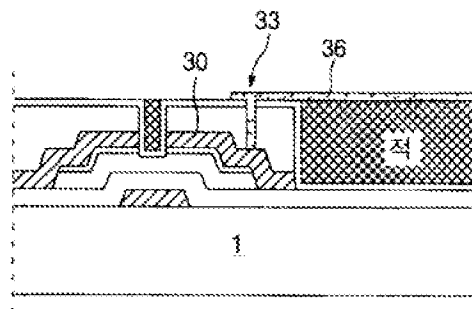
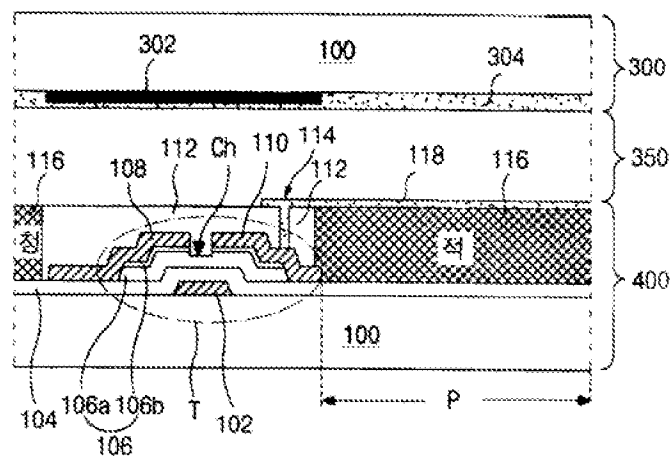


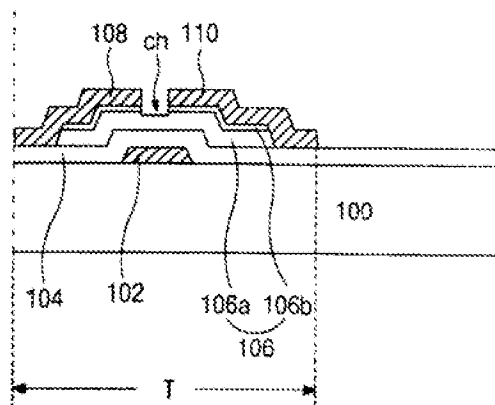
圖 2e



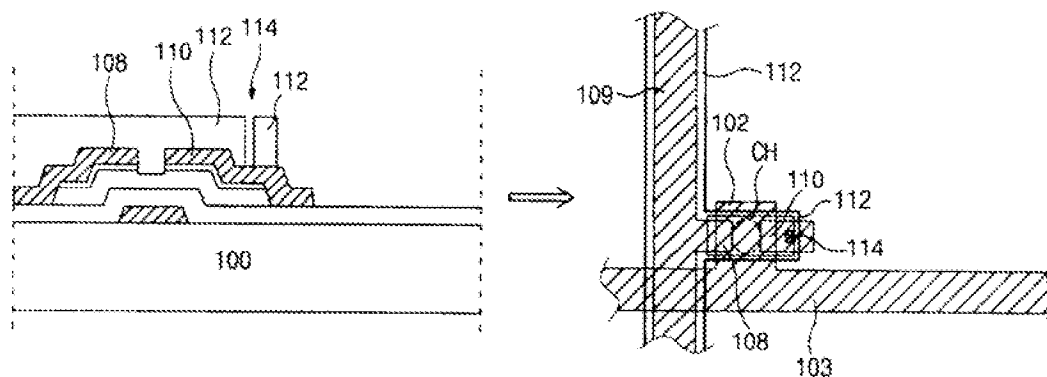
도 39



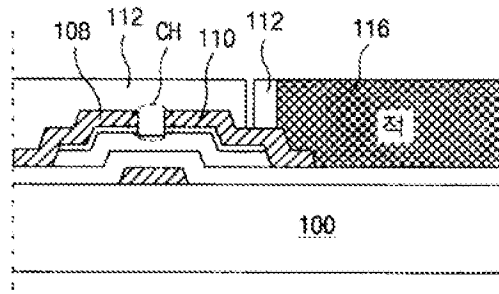
도 40a



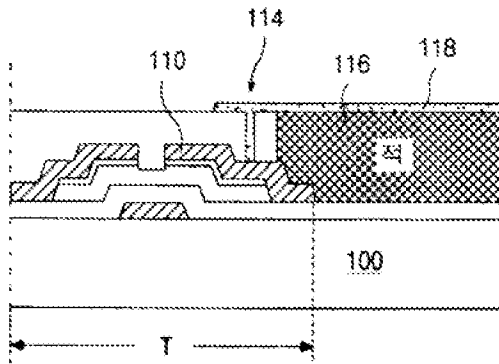
도 40b



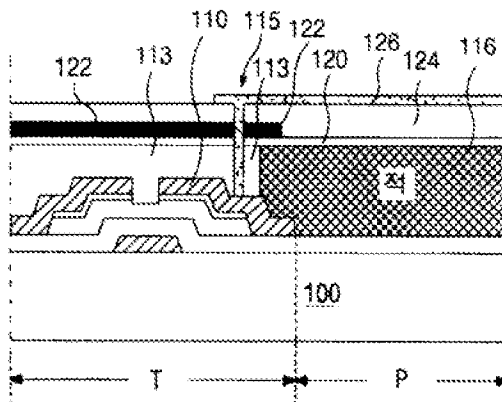
도면 4c



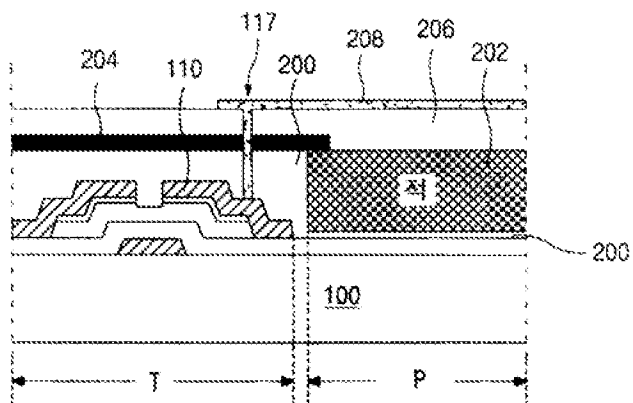
도면 4d



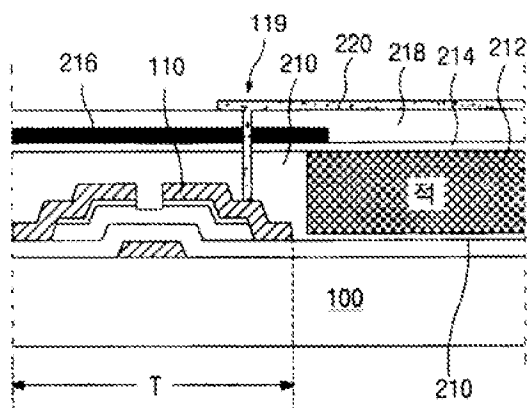
도면 5



도 6



도 7



PATENT ABSTRACTS OF JAPAN

(11)Publication number : 2002-214614

(43)Date of publication of application : 31.07.2002

(51)Int.Cl. G02F 1/1337
G09F 9/30

(21)Application number : 2001-009237

(71)Applicant : TOSHIBA CORP

(22)Date of filing : 17.01.2001

(72)Inventor : YAMAGUCHI TAKASHI
KAWADA YASUSHI
HARUHARA KAZUYUKI
KURAUCHI SHOICHI
MANABE ATSUYUKI
MAYA NATSUKO
MURAYAMA AKIO

(54) LIQUID CRYSTAL DISPLAY

(57)Abstract:

PROBLEM TO BE SOLVED: To provide a liquid crystal display of MVA mode, in which the loss of transmissivity and the delay of response due to disclination are suppressed.

SOLUTION: This liquid crystal display 1 has a couple of substrates which are arranged opposite to each other, a pixel electrode 10 which is formed on the opposite surface of one of them, a ridge-shaped projection part 18 which is formed on the opposite surface of the other substrate, a liquid crystal layer which is inserted between the substrates, and alignment films which are formed on those substrates in contact with the liquid crystal layer, and the alignment film formed on the opposite surface of another substrate has, on its surface, a ridge-shaped projection structure corresponding to the ridge-shaped projection part 18 and then areas which are mutually different in the alignment direction of liquid crystal molecules are formed in the liquid crystal layer. The ridge-shaped projection part 18 extends crossing a pixel area prescribed by the pixel electrode 10 and also crossing the sides constituting the outline of the pixel area at an acute angle and is parted at least one place in the pixel area.

